

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	13	( (ILP (instrucion dj level adj parallel\$3))) same (compil\$3 static\$4) and (((two many plurality plural other) with (fetch\$3 adj unit)) and (execut\$3 with parallel\$7))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 07:52
L8	1953	"l37" not L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 07:51
L9	1953	"l37" not L7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 07:51
L10	137	( (ILP (instruction dj level adj parallel\$3))) same (compil\$3 static\$4) and (((two many plurality plural ) with (fetch\$3 adj unit)) and (execut\$3 with parallel\$7))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 07:52
L11	12	l10 and ((utiliz\$7 optimiz\$7 maximiz\$7) with (execut\$3 processing schedul\$3) with (bandwidth wide width capacit\$3 capabilit\$7))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:03
L13	36	schedul\$3 with (determin\$7) with (execut\$3 processing) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:01
L14	70	(determin\$7) with (execut\$3 processing) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7) with parallel\$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:27
L15	66	l14 not l13	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:27

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L17	18	schedul\$3 with (evaulat\$3 exam\$5 calculat\$3 compute analyz\$3) with (execut\$3 processing) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:40
L18	130	(evaulat\$3 exam\$5 calculat\$3 compute analyz\$3) with (execut\$3 processing) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7) with parallel\$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:14
L19	0	((multithread\$3 multi-thread\$3 (multiple near3 thread\$3))) same (evaulat\$3 exam\$5 calculat\$3 compute analyz\$3) with (execut\$3 processing) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7) with parallel\$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:43
L20	0	((multithread\$3 multi-thread\$3 (multiple near3 thread\$3))) and (evaulat\$3 exam\$5 calculat\$3 compute analyz\$3) with (execut\$3 processing) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7) with parallel\$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:43
L21	77	l18 and ((schedul\$3 execut\$3 run\$4) with (thread\$3 instructions task process) with (simultaneous\$2 parallel\$3 concurrent\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 08:46
L22	0	schedul\$3 with (fully with (utiliz\$7 optimiz\$7 maximiz\$7)) with (execut\$3 processing processor) with (unit element) with (resource bandwidth wide width capacit\$3 capabilit\$7)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:03
L23	4	((fully with (utiliz\$7 optimiz\$7 maximiz\$7)) with (execut\$3 processing processor) with (unit element) with (resource bandwidth wide width capacit\$3 capabilit\$7)) same (parallel\$3 with execut\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:15
L24	151	(evaulat\$3 exam\$5 calculat\$3 compute analyz\$3) with (execut\$3 processing processor cpu) with (unit element) with (bandwidth wide width capacit\$3 capabilit\$7) with parallel\$7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:14
L25	0	l24 and ((fully with (utiliz\$7 optimiz\$7 maximiz\$7)) with (execut\$3 processing processor cpu) with (unit element) with (resource bandwidth wide width capacit\$3 capabilit\$7))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:17

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L26	0	l24 and ((fully with (utiliz\$7 optimiz\$7 maximiz\$7)) with (execut\$3 processing processor cpu) )	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:18
L27	4	l10 and ((fully with (utiliz\$7 optimiz\$7 maximiz\$7)) with (execut\$3 processing processor cpu) )	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:18
L28	4	l27 not l23	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT ; IBM_TDB	OR	ON	2007/12/18 10:18



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DE Culler, JP Singh - 1999 - eng.ku.ac.th

... while computer system designers must understand how best to **utilize** modern microprocessor ... 396 6.6.6 Sun **Processor** and Memory ... 692 10.3.1 **Fully** connected network ...

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GZ Chrysos, J Dean, JE Hicks, CA Waldspurger, WE ... - US Patent 6,195,748, 2001 - Google Patents

... to Support Profile-Driven **Optimization**, Proceedings of ... Tullsen et al., Simultaneous **Multithreading**: Maximizing On ... in a Symmetric Multiple **Processor** Envi-ronment ...

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I Ahmad, Y He, ML Liou - **Parallel** Computing, 2002 - Elsevier

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LA Barroso, K Gharachorloo, R McNamara, A Nowatzky ... - portal.acm.org

... shown that techniques such as simultaneous **multithreading** (SMT) can ... In a **fully** populated

Piranha chip, we have as ... that replies to the waiting **processor** at that ...

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B He, NK Govindaraju, Q Luo, B Smith - sc07.supercomputing.org

... Compared with the CPU, the GPU typically has a ... Since the estimation and **optimization**

techniques are similar on ... of the larger sizes, which **fully utilize** the bus ...

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JL Lo - 1998 - citeseer.csail.mit.edu

... 6 **full** advantage of the dynamic **resource** sharing provided ... Simultaneous **multithreading** ...

A simultaneous **multithreaded** **processor** provides several hardware contexts ...

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»

RA Dua, B Lokhande - princeton.edu

... In traditional **multithreaded** processors and superscalar processors, even ... in groups equal to the **multithreading** level ... out an analysis of the SMT processor at the ...

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AE Snively - 2000 - sdsc.edu

... weighted speedup of several jobmixes, **multithread**-ing levels ... **threading** is that unused

cycles may be devoted to ... related work on hardware **multithreading** is given ...

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L Eggert - people.nokia.net

... Likewise, the CPU-bound process will not require disk access often, so the disk-bound one can **utilize** the disk almost **fully**. The net effect is that the ...

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KA Ross, J Cieslewicz, J Rao, J Zhou, N Bandi, C ... - Ann Arbor - research.microsoft.com

... from the child operator or it collects a **full** array of ... hardware-implemented threads on the same CPU (simultaneous **multithreading**—SMT), or ... Q2 Q1 CPU **schedule** ...

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